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PATENT

005702-20068

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re application of:
Tomomi MOMOHARA

Art Unit: 2784

Serial No: Not yet assigned

Examiner: H. Nguyen

Filed: February 23, 2000

For: **SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE AND METHOD
OF TESTING THE SAME**

#2
1-19-00
L. HALLMAN

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

Box PATENT APPLICATION
Assistant Commissioner for Patents
Washington, D.C. 20231

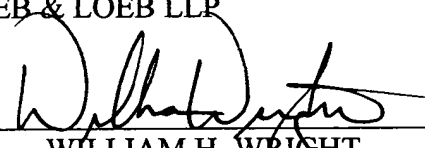
Dear Sir:

The information disclosure statement submitted herewith is being filed concurrently with the subject application [37 C.F.R. § 1.97(b)] and contains no items of information cited in any communication from a foreign patent office in a counterpart foreign application [37 C.F.R. § 1.97(e)(1)].

If it should be determined that for any reason either an insufficient or excessive fee has been paid, please charge any insufficiency or credit any overpayment necessary to ensure consideration of the information disclosure statement for the above-identified application to Deposit Account No. 12-1820. A copy of this paper is enclosed.

Respectfully submitted,
LOEB & LOEB LLP

Date: 23 February 2000

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